



# Am186™ER and Am188™ER

## High-Performance, 80C186- and 80C188-Compatible, 16-Bit Embedded Microcontrollers with RAM

This document amends the *Am186™ER and Am188™ER Microcontrollers Data Sheet* (order #20732C).

This amendment consists of one documentation change affecting the hardware watchdog timer on reset. The documentation should state the following:

- The Am186ER and Am188ER hardware watchdog timer is *inactive* after reset.

Table 1 lists the documentation changes.

**Table 1. Corrections to the *Am186™ER and Am188™ER Microcontrollers Data Sheet, Rev. C***

Subheading	Page	Original Text	Change To
WATCHDOG TIMER	49	The WDT is active after reset.	The WDT is inactive after reset.
	49	After reset, the WDT is enabled and the timeout period is set to its maximum value.	[Delete sentence.]
	49	<b>Note:</b> <i>The Watchdog Timer (WDT) is active after reset.</i>	<b>Note:</b> <i>The Am186ER and Am188ER hardware watchdog timer is inactive after reset.</i>